

1c903 U.S. PTO
09/880749

09/12/01

714 724	Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

SA SCANNED <i>Eda</i> O.A. <i>Am</i>	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/880749	CONT/PRIOR D	CLASS 714	SUBCLASS 724	ART UNIT 2838	EXAMINER J. KERVEROS
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APPLICANTS

Andrew Draper

2133

TITLE

JTAG mirroring circuitry and methods

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
714	724	714	726					
INTERNATIONAL CLASSIFICATION								
G01R 31/28								

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL
DISCLAIMER

DRAWINGS

CLAIMS ALLOWED

<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	Sheets Drwg. 10 Figs. Drwg. 16 Print Fig. 6	Total Claims 29 Print Claim for O.G. 1
	NOTICE OF ALLOWANCE MAILED 4/26/04 KERVEROS, JAMES (Assistant Examiner) (Date)	
	(Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date) _____	ISSUE FEE Amount Due _____ Date Paid _____
		ISSUE BATCH NUMBER

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